



N 10/601,287

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	IWANISHI et al.	Examiner:	Dimyan, Magid Y.
Serial No.:	10/601,287	Group Art Unit:	2825
Filed:	June 20, 2003	Docket No.:	10873.1199US01
Title:	METHOD FOR DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT		

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on September 21, 2005.

By: 

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Dear Commissioner:

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PATENT TRADEMARK OFFICE

Dated: September 21, 2005

Respectfully submitted,

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